Attorney's Docket No. 149 0144

#5/4-28-00 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Max, M. Yeung et al.

Serial No.:

09/975,293

Art Unit:

2133

Filed:

October 11, 2001

Examiner:

Torres, J.

For:

CONSTRUCTION OF AN OPTIMIZED SEC-DED CODE AND LOGIC

FOR SOFT ERRORS IN SEMICONDUCTOR MEMORIES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **SUBMISSION OF DRAWING AMENDMENT (37 CFR 1.121)**

RECEIVED

Attached please find

(check applicable items)

APR 2 0 2004

**Technology Center 2100** 

[X] 6 replacement drawing sheets in permanent ink

[ ] annotated marked-up copy of the amended drawing(s) with annotations showing the change(s).

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Dated: <u>April 7, 2004</u>

## **CERTIFICATE OF MAILING (37 CFR 1.8(a))**

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Jan M. Dunbar

Date: April 7, 2004